

PC Parallel Port JTAG Programmer

Hardware Features

- 5V or 3V3 operation.
- Adapter fits to a standard PC parallel port.
- Provides a standard 4 pin JTAG port.

Software Features

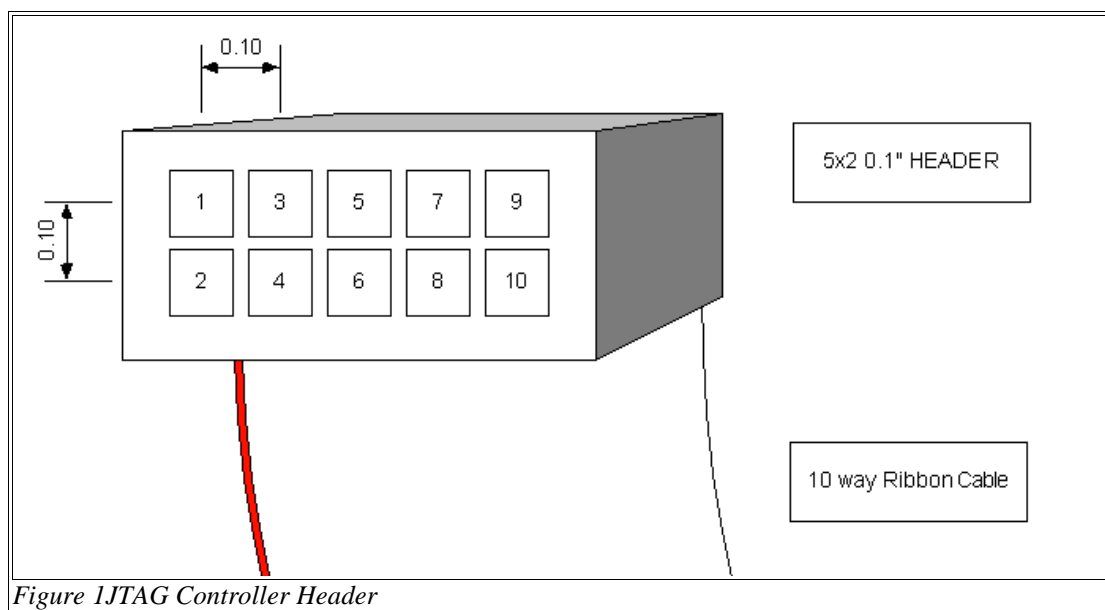
- Software provided to program Altera and devices compliant with the STAPL standard (JEDEC standard (JESD-71)).

Hardware Requirements

- PC with Parallel Port compliant to IEEE 1284.
- The programming board must supply Vcc and Ground to the JTAG Programmer.
- 10 pin header for connection to the programming board. See Figure1 for details.

Software Requirements

- Software will run on Win95/98/Me operating systems.
- Download files have to be converted to the JAM format (<http://www.jamisp.com/>).
- Altera, Xilinx and Lattice all provide utilities for doing this file conversion.



<i>Pin</i>	<i>Name</i>	<i>Description</i>	<i>Pin</i>	<i>Name</i>	<i>Description</i>
1	TCK	JTAG CLOCK	2	GND	SIGNAL GROUND
3	TDO	DATA FROM BOARD	4	VCC	POWER SUPPLY
5	TMS	JTAG CONTROL	6	NC	
7	NC		8	NC	
9	TDI	DATA TO BOARD	10	GND	SIGNAL GOUND